

Bits	Search Text	Time Stamp
1	test and (parasitic adj inductance) and (lc with oscillator) and varactor and frequency	US-PGPUB; 2005/05/18 12:11
2	0 (test with structure) and (parasitic adj inductance) and (lc with oscillator) and varactor	US-PGPUB; 2005/05/18 11:32
3	0 (test with structure) and (parasitic adj inductance) and (lc with oscillator) and varactor	US-PGPUB; 2005/05/18 11:32
4	17 test and (parasitic adj inductance) and varactor and frequency	US-PGPUB; 2005/06/06 11:09
5	9599 257/19, 223, 3224, 666, 881/103, 666, 300, 632, 649, 654, 635, 636, 657, 666	US-PGPUB; 2005/05/18 12:04
6	5510 {parasitic adj inductance}	US-PGPUB; 2005/05/18 12:47
7	2 95 and S6 and varactor	US-PGPUB; 2005/06/06 12:47
8	52 95 and S6	US-PGPUB; 2005/05/18 12:47
9	935 {parasitic adj inductance}.ab.	US-PGPUB; 2005/05/18 12:47
10	3 59 and S5	US-PGPUB; 2005/05/18 12:47
11	1 {parasitic adj inductance} and substructure	US-PGPUB; 2005/05/18 12:47
12	2 S12 and S5	US-PGPUB; 2005/05/18 12:47
13	67 {parasitic adj inductance} and varactor	US-PGPUB; 2005/05/18 12:47
14	17 {parasitic adj inductance} and varactor and test	US-PGPUB; 2005/05/18 12:47
15	17 S15 not S14	US-PGPUB; 2005/05/18 12:47
16	17 (measure or measurement) with inductance) and varactor and test	US-PGPUB; 2005/05/18 12:47
17	2 on-chip adj parasitic adj inductance	US-PGPUB; 2005/06/06 11:06
18	0 (test adj structure) and (integrated adj circuit) and (semiconductor adj wafer) and (lc adj oscillator) and (parasitic adj inductance)	US-PGPUB; 2005/06/06 11:06
19	0 (test ) and (integrated adj circuit) and (semiconductor adj wafer) and (lc adj oscillator) and (parasitic adj inductance)	US-PGPUB; 2005/06/06 11:09
20	0 (integrated adj circuit) and (semiconductor adj wafer) and (lc adj oscillator) and (parasitic adj inductance)	US-PGPUB; 2005/06/06 11:09
21	0 (semiconductor adj wafer) and (lc adj oscillator) and (parasitic adj inductance)	US-PGPUB; 2005/06/06 11:09
22	0 wafer and (lc adj oscillator) and (parasitic adj inductance)	US-PGPUB; 2005/06/06 11:09
23	17 test and (parasitic adj inductance) and varactor and frequency	US-PGPUB; 2005/06/06 11:09
24	9 (test adj structure) and (integrated adj circuit) and (semiconductor adj wafer) and (parasitic adj inductance)	US-PGPUB; 2005/06/06 11:09
25	0 (test adj structure) and (integrated adj circuit) and (semiconductor adj wafer) and (parasitic adj inductance) and lc	US-PGPUB; 2005/06/06 11:09
26	5 (integrated adj circuit) and (semiconductor adj wafer) and (parasitic adj inductance) and lc	US-PGPUB; 2005/06/06 11:10
27	4 (semiconductor adj wafer) and (parasitic adj inductance) and lc	US-PGPUB; 2005/06/06 11:10
28	59 (semiconductor adj wafer) and (parasitic adj inductance) and test	US-PGPUB; 2005/06/06 11:10
29	18 (semiconductor adj wafer) and (parasitic adj inductance) and (test adj (structure or circuit))	US-PGPUB; 2005/06/06 11:31
30	59 (semiconductor adj wafer) and (parasitic adj inductance) and test and inductance	US-PGPUB; 2005/06/06 12:20
31	3 varactor and (semiconductor adj wafer) and (parasitic adj inductance)	US-PGPUB; 2005/06/06 12:48
32	26 (parasitic adj inductance) and (integrated adj circuit) and (oscillation same frequency) and test	US-PGPUB; 2005/06/06 15:13
33	0 (parasitic adj inductance) and (integrated adj circuit) and (oscillation same frequency) and test and (voltage with sweep)	US-PGPUB; 2005/06/06 15:25
34	23 (parasitic adj inductance) and (integrated adj circuit) and (oscillation same frequency) and test and (water or substrate)	US-PGPUB; 2005/06/06 15:25

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... in Figure 7 where the **inductance** and quality factor between **measurement** and

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... The **effects of on-chip parasitic line inductance** have recently ... [1] PJ

Restle et al., "Measurement and modeling of **on-chip** transmission ...

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### On-chip inductance modeling and analysis

... and analyzing the **effects of parasitic inductance** on power grid noise, ...

Kaustav Banerjee , Amit Mehrotra, Analysis of **on-chip inductance effects** ...

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... explicitly emphasizing the **parasitic inductance** and reducing the ... sion-line

effects important for **on-chip** interconnections?", IEEE Trans. ...

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... The package's **parasitic inductance** increases at the corner pins because the lead

... size and the difficulty of obtaining a high **on-chip** quality factor. ...

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... current **measurement**, one must consider the **parasitic inductance** of the ...

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... For same **inductance**, the solenoid inductor has. smaller total **parasitic** capacitance than ... the skin effect which decreases the internal **inductance**. ...

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